

The Graz kHz FPGA Card now includes:

- 2 event timers: Medium resolution, 250 ps RMS; fast: 20 ns;
 - Allows simple and fast calculation of next Range Gate, residuals for display;
 - No need to wait for a slow / external (high accuracy) Event Timer
- A 500 ps resolution, completely digital Range Gate Generator
 - Replaces previous analog delay chip; better linearity etc.

Epoch Time Differences (FPGA - EventTimer)

